

**CUSTOM CLOCK INTERCONNECTS  
ON STANDARDIZED SILICON PLATFORM  
ABSTRACT OF THE DISCLOSURE**

5           A standardized silicon platform chip has a substrate surface with  
an array of unconnected transistors that surround islands. The islands  
have circuit elements that are interconnectable within each island to  
form a plurality of varied circuit functions for each of the islands. The  
varied circuit functions include both application functions and clock  
10 functions. Interconnect layers are deposited over the substrate surface to  
interconnect the circuit elements within each island to complete the  
varied circuit functions. The varied circuit functions include varied levels  
of integration including at least gates, flip-flops, clock trees, and  
oscillators. The varied circuit functions are custom connectable to the  
15 array of unconnected transistors to form standard clock resources for the  
standardized silicon platform chip.